## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

METHODS FOR	Maitreyee Mahajani et al. 10/079,472 February 19, 2002 GATE DIELECTRIC OR INTEGRATED CIRCUITS AND MAKING AND USING SUCH RIC STRUCTURES	)
	POWER OF ATTORNEY (REVOC	CATION OF PRIOR POWERS)
Commissioner for P.O. Box 1450 Alexandria, VA 2		•
Sir:		
and appoints all at	torneys and agents associated with Cu entified above, and to transact all busine	f attorney given in the above-identified application stomer Number 67558 as our attorneys to prosecute ass in the United States Patent and Trademark Office
		ddress for the above-identified application to the irect any telephone calls to (216) 502-0600.
above-identified p	patent application. We, the undersignence establishing chain of title to the pa	e assignee of the entire right, title and interest in the ned, declare that we have reviewed copies of the atent application identified above from the inventors
<u> </u>	is filed for recordation herewith; or was recorded at Reel 018950 and Fra a copy is attached herewith.	ume 0686; or
The under By SanDis	signed are authorized to sign this docur k Corporation as its sole member	<u> </u>
(1) Signati	ure: <u>Ina K. Sett</u> 1019107	<b>/</b>
Date:	10/9/07	
Name:		

Title: Senior Director & Associate Chief IP Counsel